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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/693,378	10/23/2003	Chris D. Hyser	200205369-1	1637
	7590 12/28/2007 CKARD COMPANY		EXAM	INER
P O BOX 272400, 3404 E. HARMONY ROAD INTELLECTUAL PROPERTY ADMINISTRATION			ALMEIDA, DEVIN E	
<del>-</del>	FORT COLLINS, CO 80527-2400			PAPER NUMBER
			2132	-
			NOTIFICATION DATE	DELIVERY MODE
			12/28/2007	ELECTRONIC

## Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

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•	Application No.	Applicant(s)			
	10/693,378	HYSER, CHRIS D.			
Office Action Summary	Examiner	Art Unit			
	Devin Almeida	2132			
The MAILING DATE of this communication app Period for Reply	ears on the cover sheet with the c	orrespondence addres	s		
A SHORTENED STATUTORY PERIOD FOR REPLY WHICHEVER IS LONGER, FROM THE MAILING DA  - Extensions of time may be a vailable under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication.  - If NO period for reply is specified above, the maximum statutory period of Failure to reply within the set or extended period for reply will, by statute Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COMMUNICATION 36(a). In no event, however, may a reply be tin will apply and will expire SIX (6) MONTHS from a cause the application to become ABANDONE	N. nely filed the mailing date of this commur D (35 U.S.C. § 133).			
Status					
1)⊠ Responsive to communication(s) filed on 29 O	ctober 2007.				
•	action is non-final.				
3) Since this application is in condition for alloward closed in accordance with the practice under E			rits is		
Disposition of Claims					
4) ☐ Claim(s) 1-5 and 15-18 is/are pending in the apart 4a) Of the above claim(s) is/are withdraw 5) ☐ Claim(s) is/are allowed.  6) ☐ Claim(s) 1-5 and 15-18 is/are rejected.  7) ☐ Claim(s) is/are objected to.  8) ☐ Claim(s) are subject to restriction and/o	vn from consideration.		·		
Application Papers	10-				
9) The specification is objected to by the Examine	r				
10) ☐ The drawing(s) filed on is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.					
Applicant may not request that any objection to the	- · ·				
Replacement drawing sheet(s) including the correct	•				
11)☐ The oath or declaration is objected to by the Ex	aminer. Note the attached Oπice	Action or form P1O-1	52.		
Priority under 35 U.S.C. § 119			•		
12) Acknowledgment is made of a claim for foreign  a) All b) Some * c) None of:  1. Certified copies of the priority document  2. Certified copies of the priority document  3. Copies of the certified copies of the priority application from the International Bureau  * See the attached detailed Office action for a list	s have been received. s have been received in Applicati rity documents have been receive u (PCT Rule 17.2(a)).	on No ed in this National Stag	ge		
Attachment(s)					
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO/SB/08) Paper No(s)/Mail Date	4) Interview Summary Paper No(s)/Mail Di 5) Notice of Informal F 6) Other:	ate			

## **DETAILED ACTION**

This action is in response to the papers filed 10/29/2007. Applicant elects claims 1-5 and 15-18 with traverse.

## Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 1-5 and 15-18 are rejected under 35 U.S.C. 103(a) as being unpatentable over Colligan et al (U.S. 6,519,762) in view of Crumly (U.S. 20030161475). Colligan teaches with respect to claim 1, a method for preparing an authenticable and verifiable image of a module, the method comprising: receiving a module image (see Colligan column 9 lines 34-38); adding to the module image a size and location block (see Colligan column 9 lines 34-38). Colligan does not teach adding to the module image an authentication block including a cryptographically protected module-specific public key and a clear-text version of the module-specific public key to produce an authenticable image; and adding to the authenticable image a verification block that includes a digital signature prepared from the module image. Crumly teaches adding to the module image an authentication block including a cryptographically protected module-specific public key (see Crumly paragraph 0025) and a clear-text version of the module-specific public key to produce an authenticable image (see Crumly paragraph 0025 and 0036); and adding to the authenticable image a verification block that includes a digital

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signature prepared from the module image (see Crumly paragraph 0036). It would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains to have included a digital signature of the image to so that the user can verify that the digital image has not been altered. Therefore one would have been motivated to have included a digital signature of the image (see Crumly paragraph 0036).

With respect to claim 2, wherein adding to the module image a size and location block further includes: adding, in a specific location, a header that includes an image size, location, and globally unique identifier, that describes a size and location of the firmware image within a flash memory or other non-volatile memory, and that identifies a class of machines for which the firmware module has been created (see Colligan column 9 lines 12-38).

With respect to claim 3, wherein adding to the module image an authentication block including a cryptographically protected module-specific public key and a clear-text version of the module-specific public key to produce an authenticable image further includes: adding to the module image an authentication block including an encrypted, hashed module-specific public key and a clear-text version of the module-specific public key to produce an authenticable image (see Crumly paragraph 0025).

With respect to claim 4, wherein adding to the authenticable image a verification block that includes a digital signature prepared from the module image further includes: adding to the authenticable image a verification block that includes a digital signature

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prepared by hashing the module image and encrypting the hashed module image with a module-specific private key (see Crumly paragraph 0036).

With respect to claim 5, a computer instructions that together compose a program that carries out the method of claim 1 stored in computer readable medium (see Colligan column 9 lines 34-38).

With respect to claim 15, an authenticable and verifiable image of an a module stored in a computer-readable medium comprising: a module image, including a size, location, and globally unique-identifier block (see Colligan column 9 lines 34-38); an authentication block (see Crumly paragraph 0025 and 0036); and a verification block (see Crumly paragraph 0036).

With respect to claim 16, wherein the authentication block contains an encrypted, hashed module-specific public key and a clear-text version of the module-specific public key to produce an authenticable image (see Crumly paragraph 0025 and 0036).

With respect to claim 17, wherein the verification block that includes a digital signature prepared by hashing the module image and encrypting the hashed module image with a module-specific private key (see Crumly paragraph 0036).

With respect to claim 18, a method for preparing an authenticable and verifiable image of a module, the method comprising: a module-image receiving step (see Colligan column 9 lines 34-38); a size-and-location-data adding step that adds size-and-location data to the received module image (see Colligan column 9 lines 34-38); an authentication-adding step that adds, to the module image, authentication information including a cryptographically protected module-specific public key and a clear-text

version of the module-specific public key (see Crumly paragraph 0025 and 0036); and a verification-block-adding step that adds a digital signature prepared from the module image to the module image (see Crumly paragraph 0036).

## Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Devin Almeida whose telephone number is 571-270-1018. The examiner can normally be reached on Monday-Thursday from 7:30 A.M. to 5:00 P.M. The examiner can also be reached on alternate Fridays from 7:30 A.M. to 4:00 P.M.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Gilberto Barron, can be reached on 571-272-3799. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Devin Almeida Patent Examiner 12/18/07

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